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Performance tuning is becoming more important than it has been for the last 40 years. Read this book to understand your application's performance that runs on a modern CPU and learn how you can improve it. The 170+ page guide combines the knowledge of many optimization experts from different industries. An essential book for 3rd party developers and others interested in products using the PowerPC including those from IBM, Apple, and many other vendors. The book covers the architecture for the entire family of processors from either IBM or Motorola and is the official documentation of the IBM reference manual. The Official Reference Document to OpenGL, Version 1.4 OpenGL is a powerful software interface used to produce high-quality computer-generated images and interactive graphics applications by rendering 2D and 3D geometric objects, bitmaps, and color images. Officially sanctioned by the OpenGL Architecture Review Board (ARB), The OpenGL Reference Manual, Fourth Edition, is the comprehensive and definitive documentation of all core OpenGL functions. This fourth edition has been completely revised and updated

for OpenGL Versions 1.3 and 1.4. It features coverage of cube-mapped textures, multisampling, depth textures and shadowing, multitexturing, and register combiners. In addition, this book documents all OpenGL Utility Library functions (GLU 1.3) and the OpenGL extension to the X Window System (GLX 1.3). A comprehensive reference section documents each set of related OpenGL commands. Each reference page contains: A description of the command's parameters The command's effect on rendering and how OpenGL's state is modified Examples References to related functions Errors generated by each function This book also includes a conceptual overview of OpenGL, a summary of commands and routines, a chapter on defined constants and associated commands, and descriptions of the multitexturing and imaging subset ARB extensions. The OpenGL Technical Library provides tutorial and reference books for OpenGL. The Library enables programmers to gain a practical understanding of OpenGL and shows them how to unlock its full potential. Originally developed by SGI, the Library continues to evolve under the auspices of the Architecture Review Board (ARB), an industry consortium responsible for guiding the evolution of OpenGL and related technologies. The OpenGL ARB is composed of leaders in the computer graphics industry: 3Dlabs, Apple, ATI, Dell, Evans & Sutherland, Hewlett-Packard, IBM, Intel, Matrox, NVIDIA, SGI, and Sun Microsystems. About the ARM Architecture The ARM architecture is the industry's leading 16/32-bit embedded RISC processor solution. ARM Powered microprocessors are being routinely designed into a wider range of products than any other 32-bit processor. This wide applicability is made possible by the ARM architecture, resulting in optimal system solutions at the crossroads of high performance, low power consumption and low cost. About the book This is the authoritative reference guide to the ARM RISC architecture. Produced by the architects that are actively working on the ARM specification, the book contains detailed information about all versions of the ARM and Thumb instruction sets, the memory management and cache functions, as well as optimized code examples. 0201737191B05092001 The official guide to the 8086/8088 microprocessor family--for programmers and hardware engineers. This book is an ideal supplement to the data sheets found in Intel's Embedded Controllers and Processor Handbook. It explains operating modes of 80C186/C188 processors and the higher performance alternative, 80C186XL/C188XL. Programmers will find full coverage of interrupts, address and data bus cycles, memory and I/O interfaces, 80C187 math coprocessor, etc. Om hvordan mikroprocessorer fungerer, med undersøgelse af de nyeste mikroprocessorer fra Intel, IBM og Motorola. A number of widely used contemporary processors have instruction-set extensions for improved performance in multi-media applications. The aim is to allow operations to proceed on

multiple pixels each clock cycle. Such instruction-sets have been incorporated both in specialist DSPchips such as the Texas C62xx (Texas Instruments, 1998) and in general purpose CPU chips like the Intel IA32 (Intel, 2000) or the AMD K6 (Advanced Micro Devices, 1999). These instruction-set extensions are typically based on the Single Instruction-stream Multiple Data-stream (SIMD) model in which a single instruction causes the same mathematical operation to be carried out on several operands, or pairs of operands, at the same time. The level of parallelism supported ranges from two floating point operations, at a time on the AMD K6 architecture to 16 byte operations at a time on the Intel P4 architecture. Whereas processor architectures are moving towards greater levels of parallelism, the most widely used programming languages such as C, Java and Delphi are structured around a model of computation in which operations take place on a single value at a time. This was appropriate when processors worked this way, but has become an impediment to programmers seeking to make use of the performance offered by multi-media instruction-sets. The introduction of SIMD instruction sets (Peleg et al. In its fourth edition, this book focuses on real-world examples and practical applications and encourages students to develop a "big-picture" understanding of how essential organization and architecture concepts are applied in the computing world. In addition to direct correlation with the ACM/IEEE CS2013 guidelines for computer organization and architecture, the text exposes readers to the inner workings of a modern digital computer through an integrated presentation of fundamental concepts and principles. It includes the most up-to-the-minute data and resources available and reflects current technologies, including tablets and cloud computing. All-new exercises, expanded discussions, and feature boxes in every chapter implement even more real-world applications and current data, and many chapters include all-new examples. -- 1. General Studies Paper - 1 is the best-selling book particularly designed for the civil services Preliminary examinations. 2. This book is divided into 6 major sections covering the complete syllabus as per UPSC pattern 3. Special Section is provided for Current Affairs covering events, Summits and Conferences 4. simple and lucid language used for better understanding of concepts 5. 5 Crack Sets are given for practice 6. Practice Questions provides Topicwise Questions and Previous Years' Solved Papers With our all time best selling edition of "General Studies Manual Paper 1" is a guaranteed success package which has been designed to provide the complete coverage to all subjects as per prescribed pattern along with the updated and authentic content. The book provides the conventional Subjects like History, Geography, Polity and General Science that are thoroughly updated along with Chapterwise and Sectionwise questions. Contemporary Topics likes; Indian Economy, Environment & Ecology, Science & Technology and

General Awareness have also been explained with latest facts and figures to ease the understanding about the concepts in this book. Current events of national and international interest have been listed in a separate section. Practice Sets are given at the end, keeping in view the trend of the questions coming in exams. Lastly, More than 5000 Most Important Points for Revision are provided in the attached booklet of the guide. It is a must have tool that proves to be one point solution for the preparf Civil Services Preliminary Examination. TOC Solved Paper 2021-2018, Indian History and Indian National Movement, India and World Geography, Indian Polity and Governance, Indian Economy, General Science & Science and Technology, General Knowledge & Computer Technology, Practice: Topicwise Questions, Current Affairs, Crack Sets (1-5). Provides information on using a PC, covering such topics as hardware, networking, burning CDs and DVDs, using the Internet, and upgrading and replacing parts. Most computer users think that fiddling with the insides of their PC is taboo. They fear that by removing the screws that hold the case on, they're crossing into forbidden territory. And even for those who know they can open the box and fix or upgrade their PC, analysis paralysis often stops them in their tracks: Which upgrades offer the best bang for the buck? How do you pinpoint the faulty component that's making your system freeze? What about compatibility issues? Get ready to get unstuck and get your PC running fast and running right. Repairing and Upgrading Your PC delivers start-to-finish instructions, simple enough for even the most inexperienced PC owner, for troubleshooting, repairing, and upgrading your computer. Written by hardware experts Robert Bruce Thompson and Barbara Fritchman Thompson, this book covers it all: how to troubleshoot a troublesome PC, how to identify which components make sense for an upgrade, and how to tear it all down and put it back together. This book shows how to repair and upgrade all of your PC's essential components: Motherboard, CPU, and Memory. Choose the optimal match of these core components to keep your PC running at top speed Hard Drive, Optical Drive, and Removable Storage Give your computer what it needs for long-term and short-term storage Audio and Video. Enhance your computing experience with the right sound and graphics devices for your needs Input Devices. Pick the best keyboard and mouse to keep your hands happy and healthy Networking. Set up secure wireless networking to keep the bits flowing between your computers and the outside world Cases and Power Supplies. Keep everything running cool and reliably With its straightforward language, clear instructions, and extensive illustrations, this book makes it a breeze for PC owners of any skill level to work on their computer. This book is written for the high level

user interested in details of the i486 microprocessor architecture. The book is divided into five major sections: application programming, system programming, numeric processing, compatibility and instruction set. Intelligent readers who want to build their own embedded computer systems-- installed in everything from cell phones to cars to handheld organizers to refrigerators-- will find this book to be the most in-depth, practical, and up-to-date guide on the market. Designing Embedded Hardware carefully steers between the practical and philosophical aspects, so developers can both create their own devices and gadgets and customize and extend off-the-shelf systems. There are hundreds of books to choose from if you need to learn programming, but only a few are available if you want to learn to create hardware. Designing Embedded Hardware provides software and hardware engineers with no prior experience in embedded systems with the necessary conceptual and design building blocks to understand the architectures of embedded systems. Written to provide the depth of coverage and real-world examples developers need, Designing Embedded Hardware also provides a road-map to the pitfalls and traps to avoid in designing embedded systems. Designing Embedded Hardware covers such essential topics as: The principles of developing computer hardware Core hardware designs Assembly language concepts Parallel I/O Analog-digital conversion Timers (internal and external) UART Serial Peripheral Interface Inter-Integrated Circuit Bus Controller Area Network (CAN) Data Converter Interface (DCI) Low-power operation This invaluable and eminently useful book gives you the practical tools and skills to develop, build, and program your own application-specific computers.

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